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		RUL	E							
APPLICANTS Masahiko Hata, Tsuchiura-shi, JAPAN; Yoshinobu Ono, Tsukuba-gun, JAPAN; Kazumasa Ueda, Tsuchiura-shi, JAPAN; ** CONTINUING DATA ************************* This application is a 371 of PCT/JP04/16186 10/25/2004 ** FOREIGN APPLICATIONS ************************************										
Foreign Priority claimed 35 USC 119(a-d) conditions met Yes No Verified and Acknowledged Verified and Acknowledged			ter .nce	STATE OR COUNTRY JAPAN	SHEETS TOT CLAI					
ADDRESS FITCH, EVEN, TABIN & FLANNERY P. O. BOX 18415 WASHINGTON, DC 20036 UNITED STATES										
TITLE Method for manufacturing compound semiconductor substrate										
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